



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

*23/D

Applicant: Terletzki, *et al.* Art Unit: 2816
Serial No.: 09/659,872 Examiner: Minh Nguyen
Filed: September 13, 2000 Docket: 00P7882US
For: Level-Shifting Circuitry Having "High" Output Impedance During Disable Mode

11/4/03
Ahmth

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Preliminary Amendment Accompanying RCE

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Dear Sir:

This amendment is being filed concurrently with an RCE in response to the final rejection mailed July 25, 2003. Applicant further submits that each of the pending claims is allowable without amendment. The remarks presented on the following pages explain the rationale behind this assertion.

00P7882US